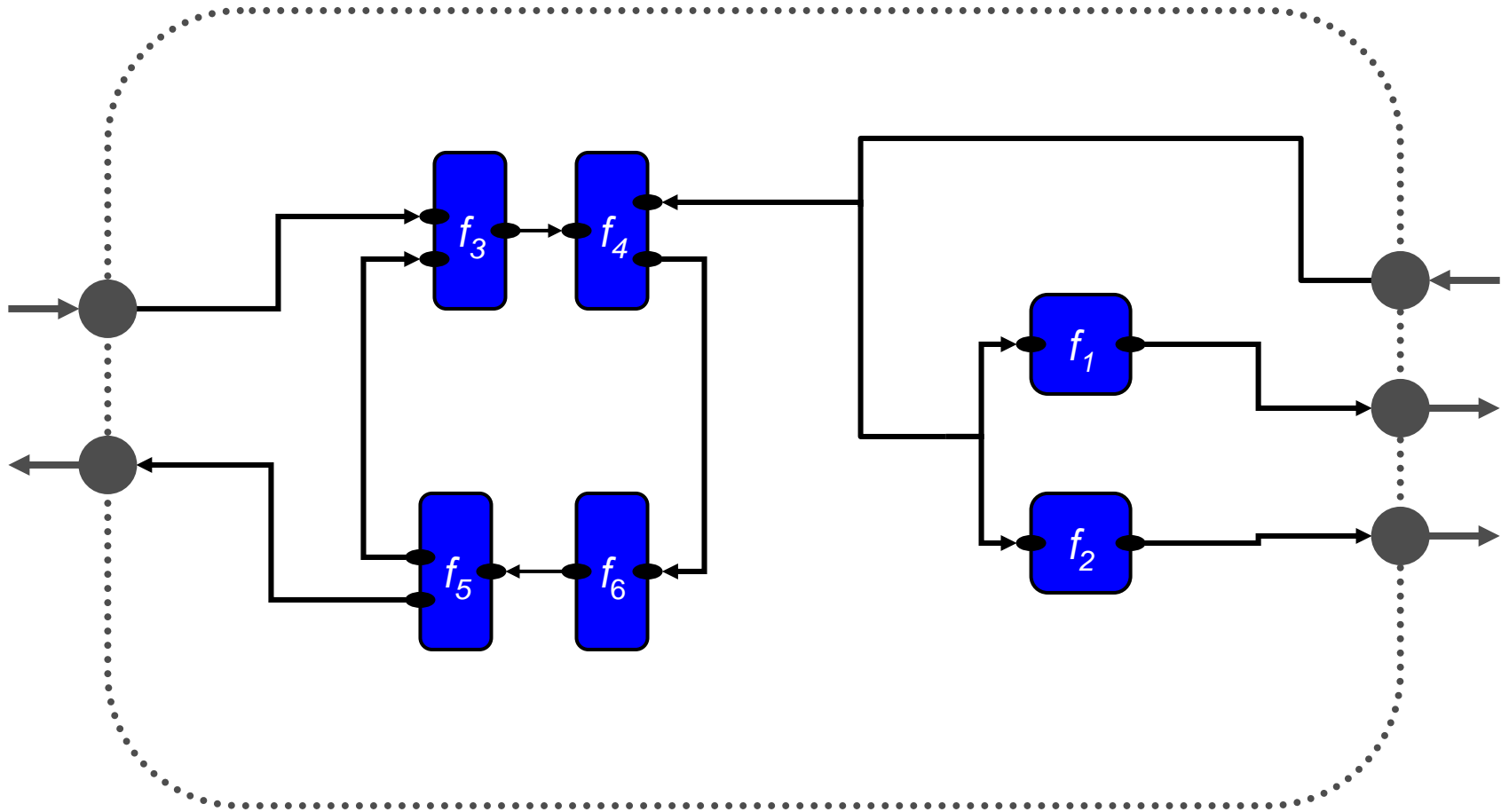


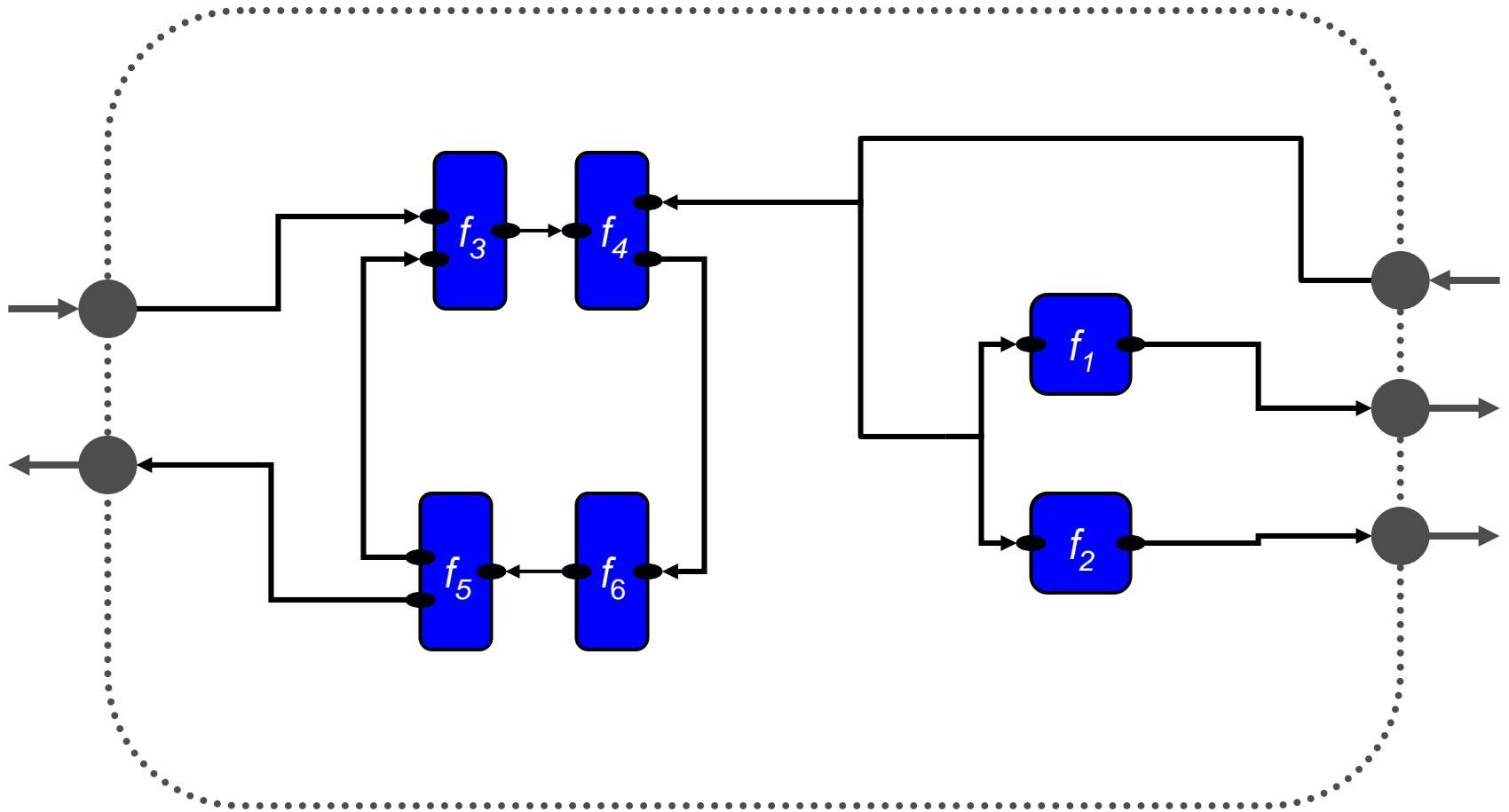
# Interface-based Design 1

Tom Henzinger  
EPFL and UC Berkeley

A Complex System is built from Components.

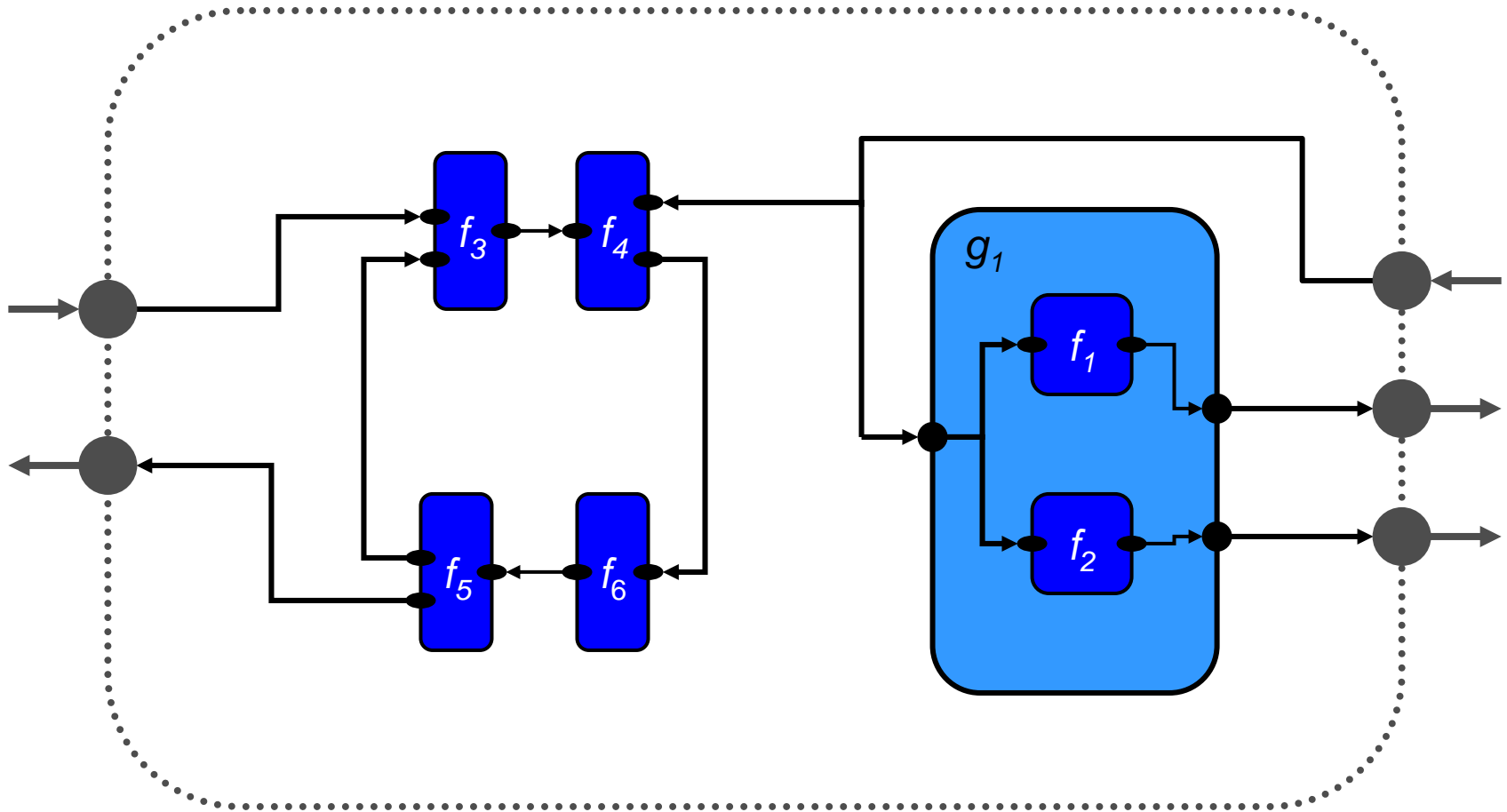


What does “Compositionality” mean?



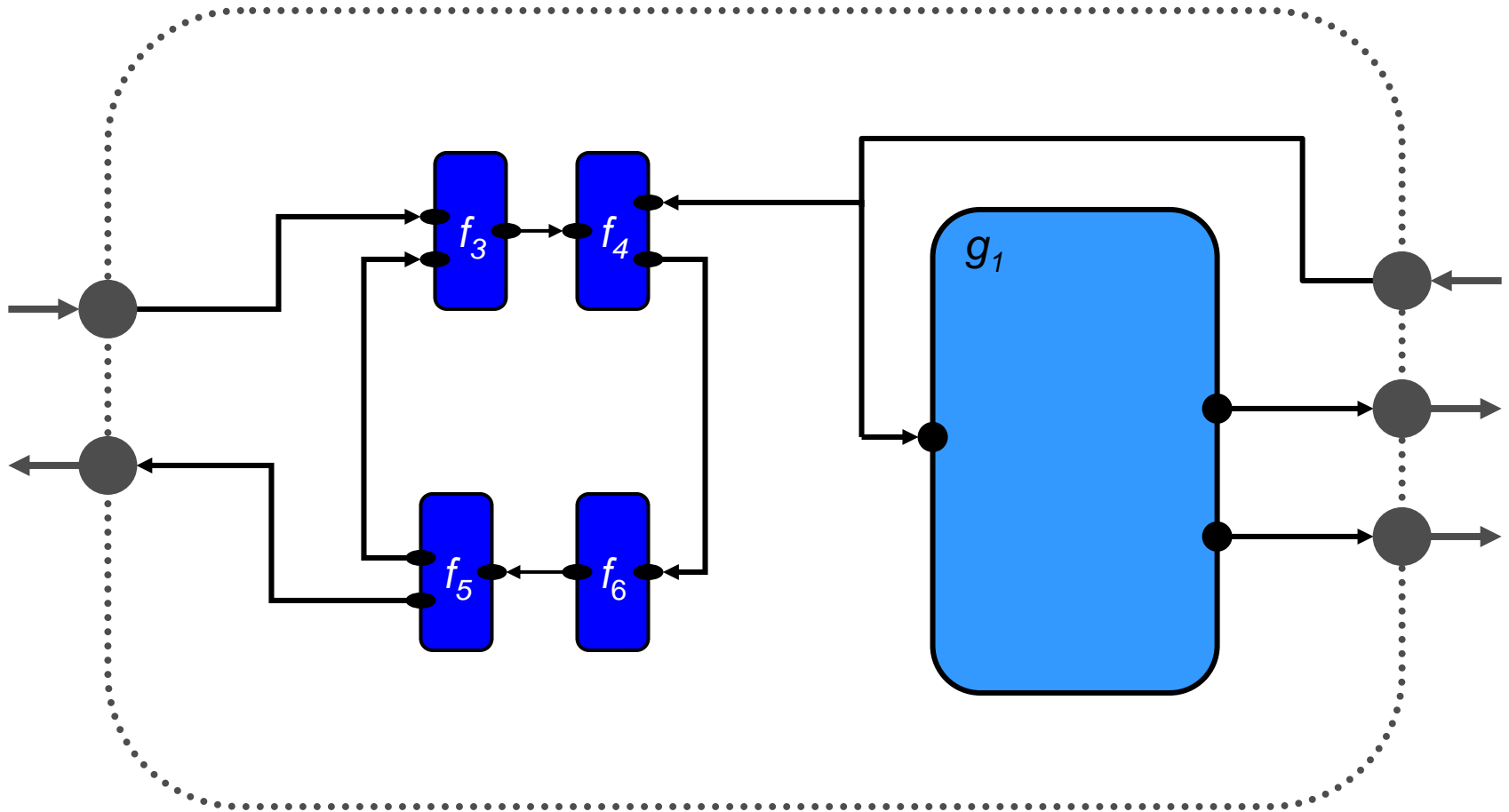
# Bottom-up Compositionality:

A collection of components is again a component.

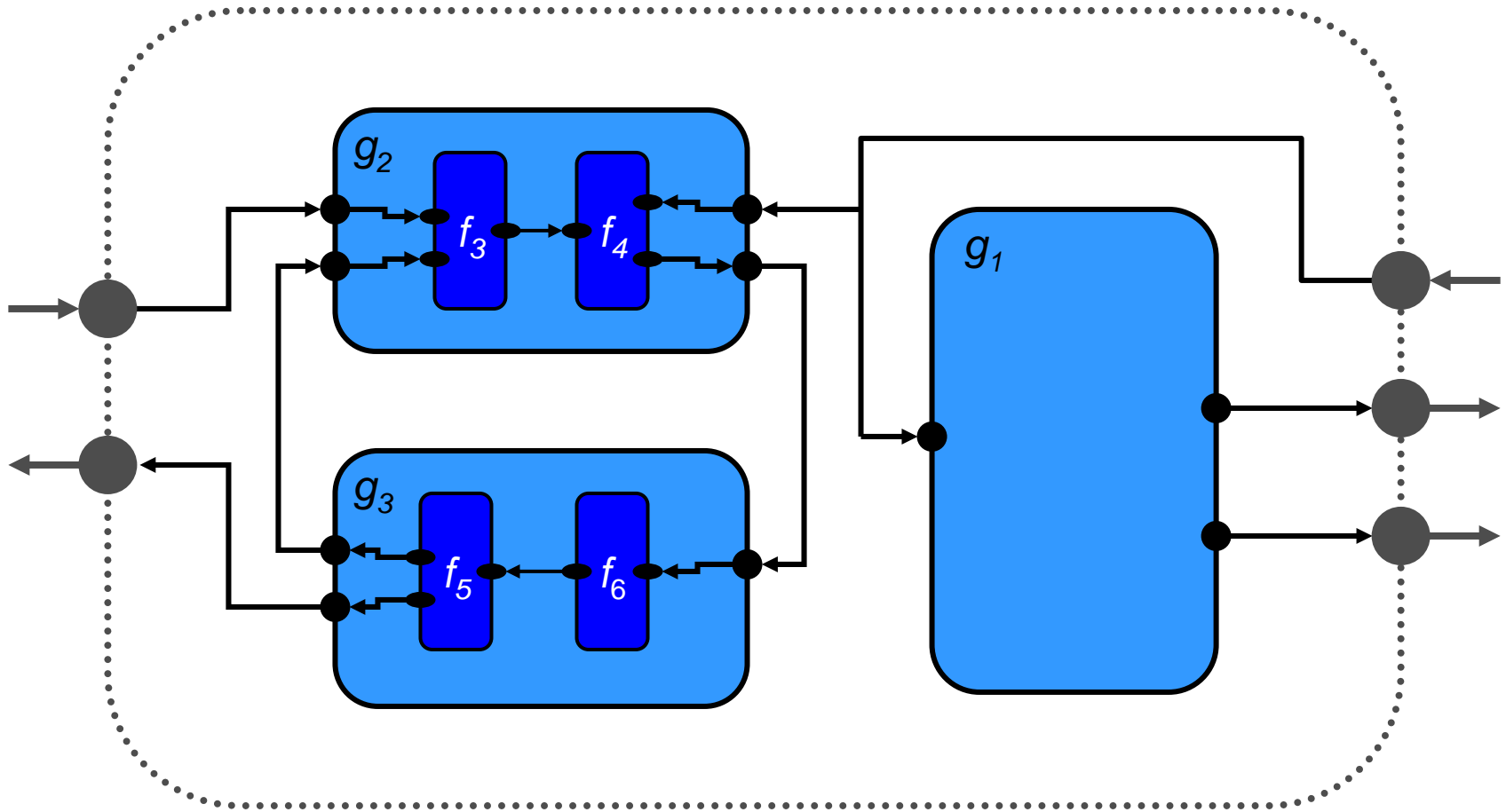


# Bottom-up Compositionality:

Supports some form of abstraction (hiding).

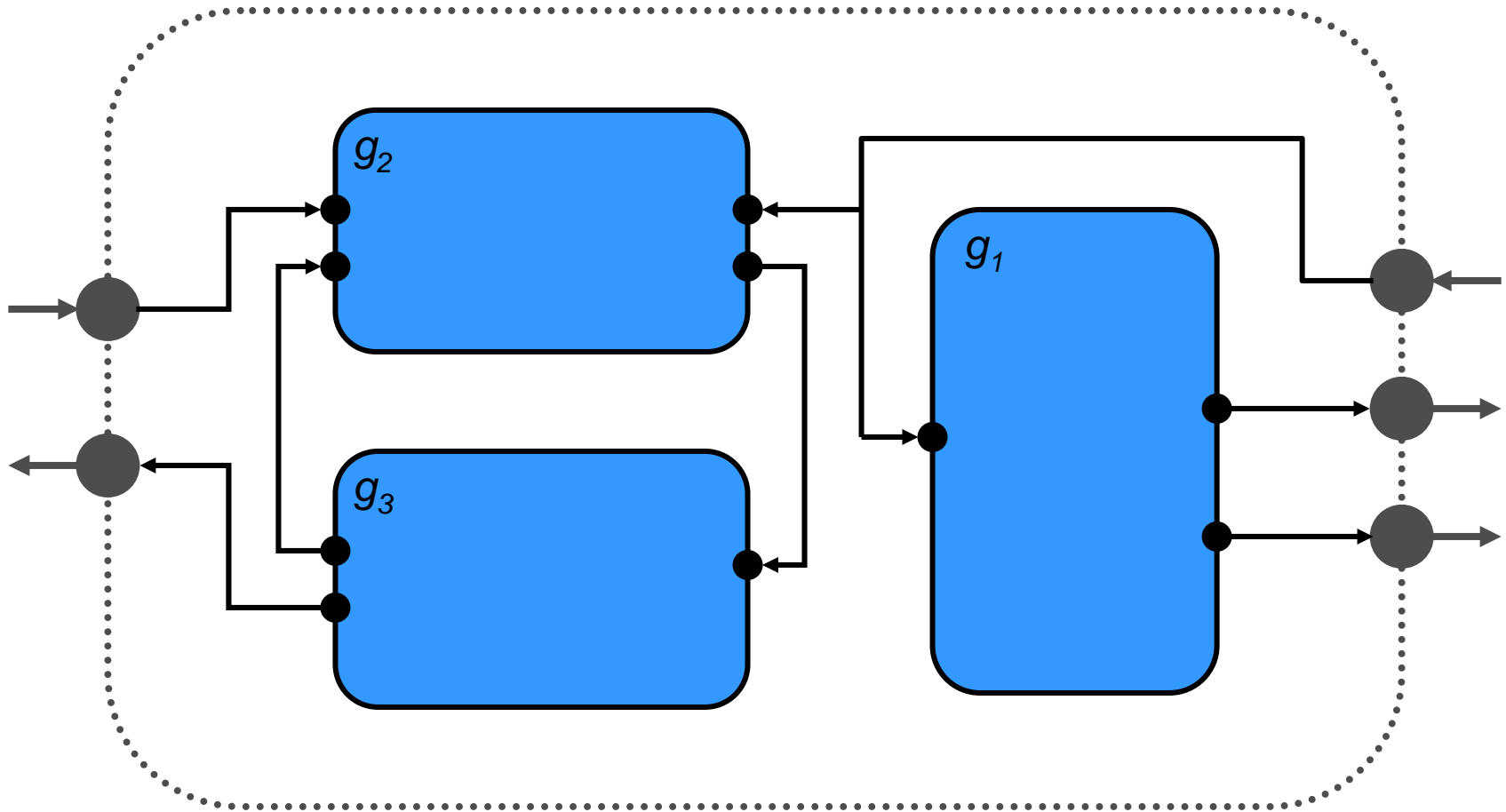


# Bottom-up Compositionality: “Properties” are also abstractions.

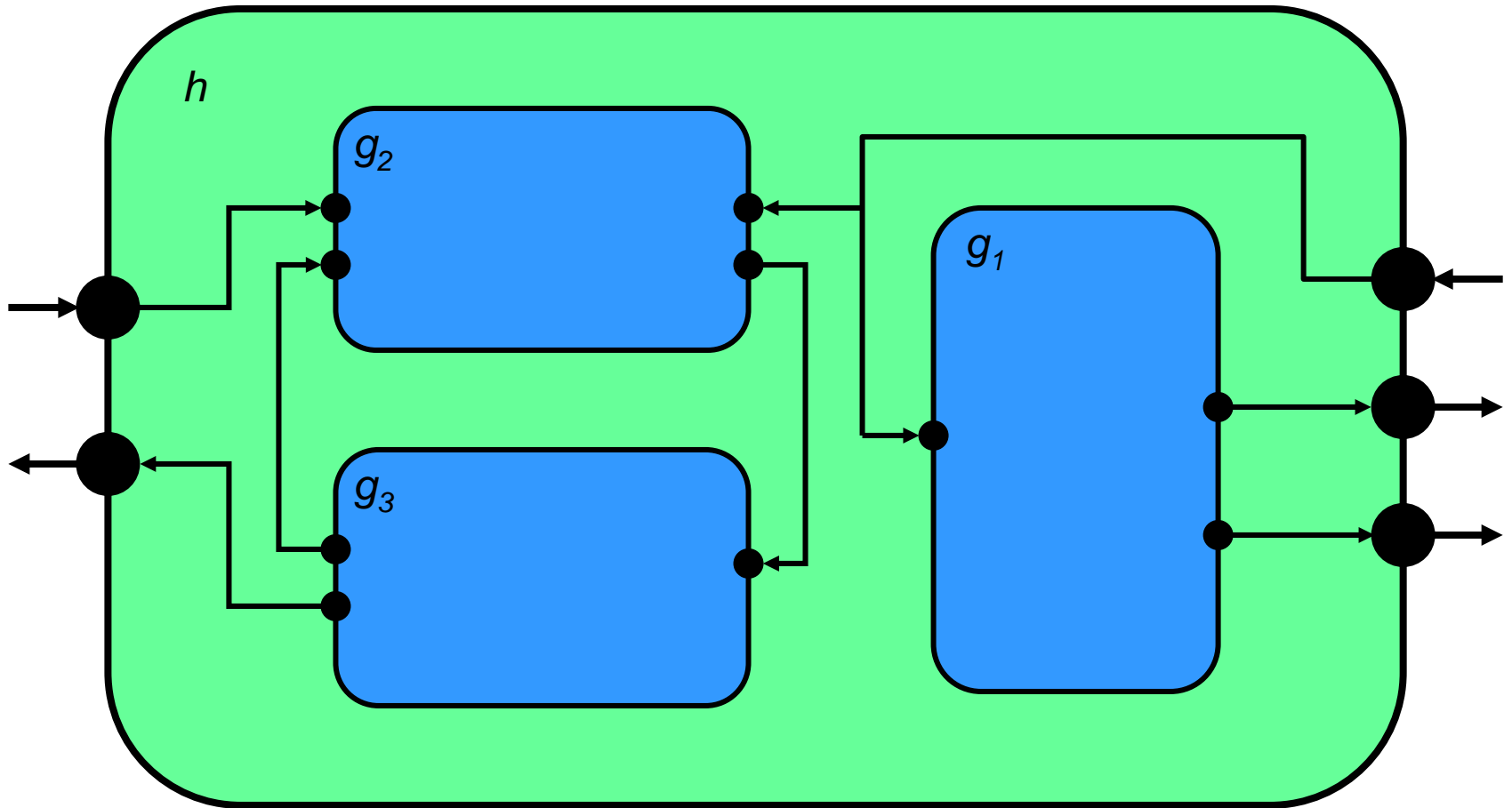


# Bottom-up Compositionality:

Abstractions/properties can again be composed.



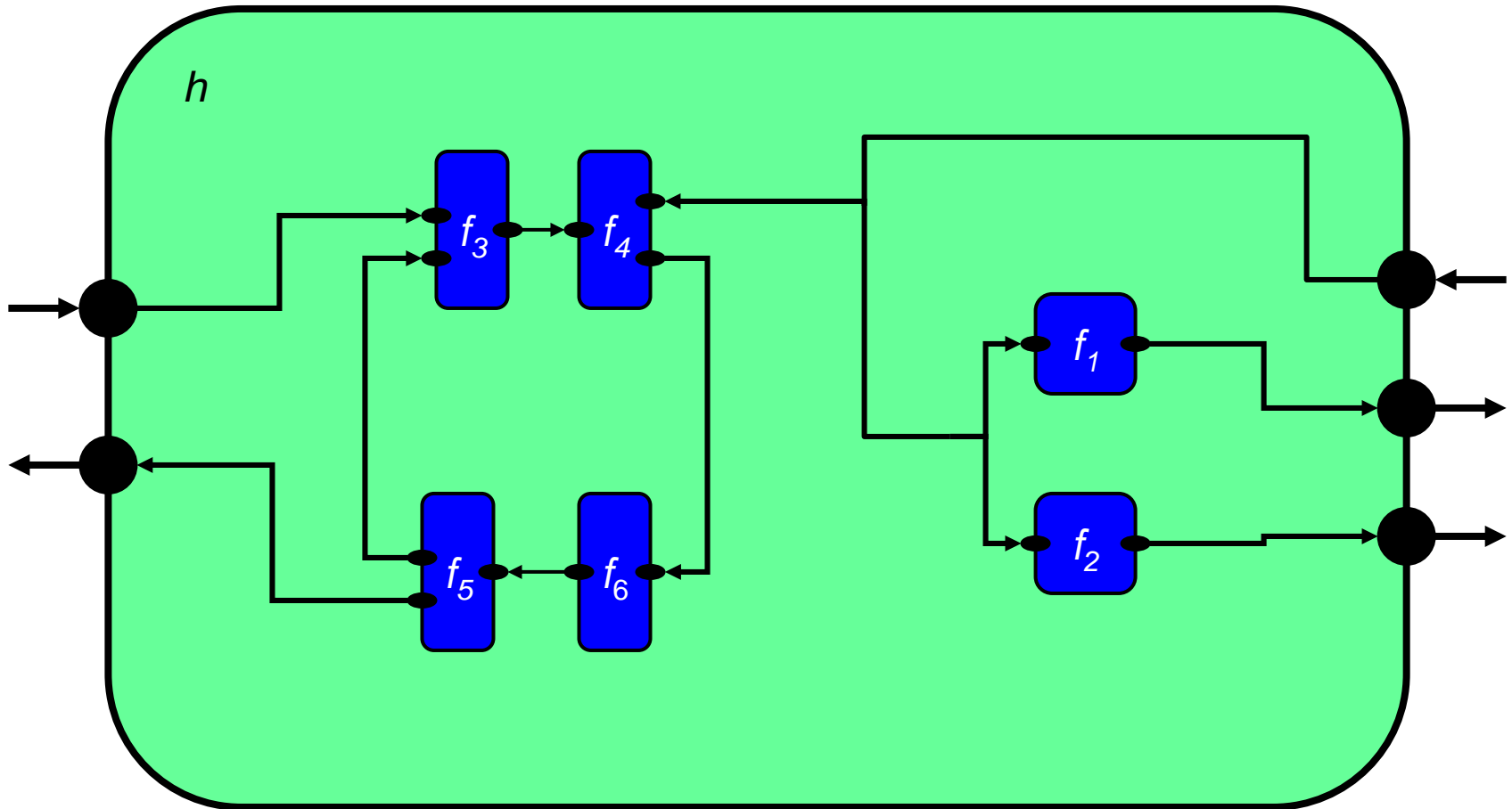
# Bottom-up Compositionality: Stepwise composition and abstraction (hierarchy).





# Bottom-up Compositionality:

If  $f \cdot g$  and  $f' \cdot g'$ , then  $f//f' \cdot g//g'$ .



# Bottom-up Compositionality: Supports Compositional Verification.

$$f_1 \parallel f_2 \cdot g_1$$

$$f_3 \parallel f_4 \cdot g_2$$

$$f_5 \parallel f_6 \cdot g_3$$

$$g_1 \parallel g_2 \parallel g_3 \cdot h$$

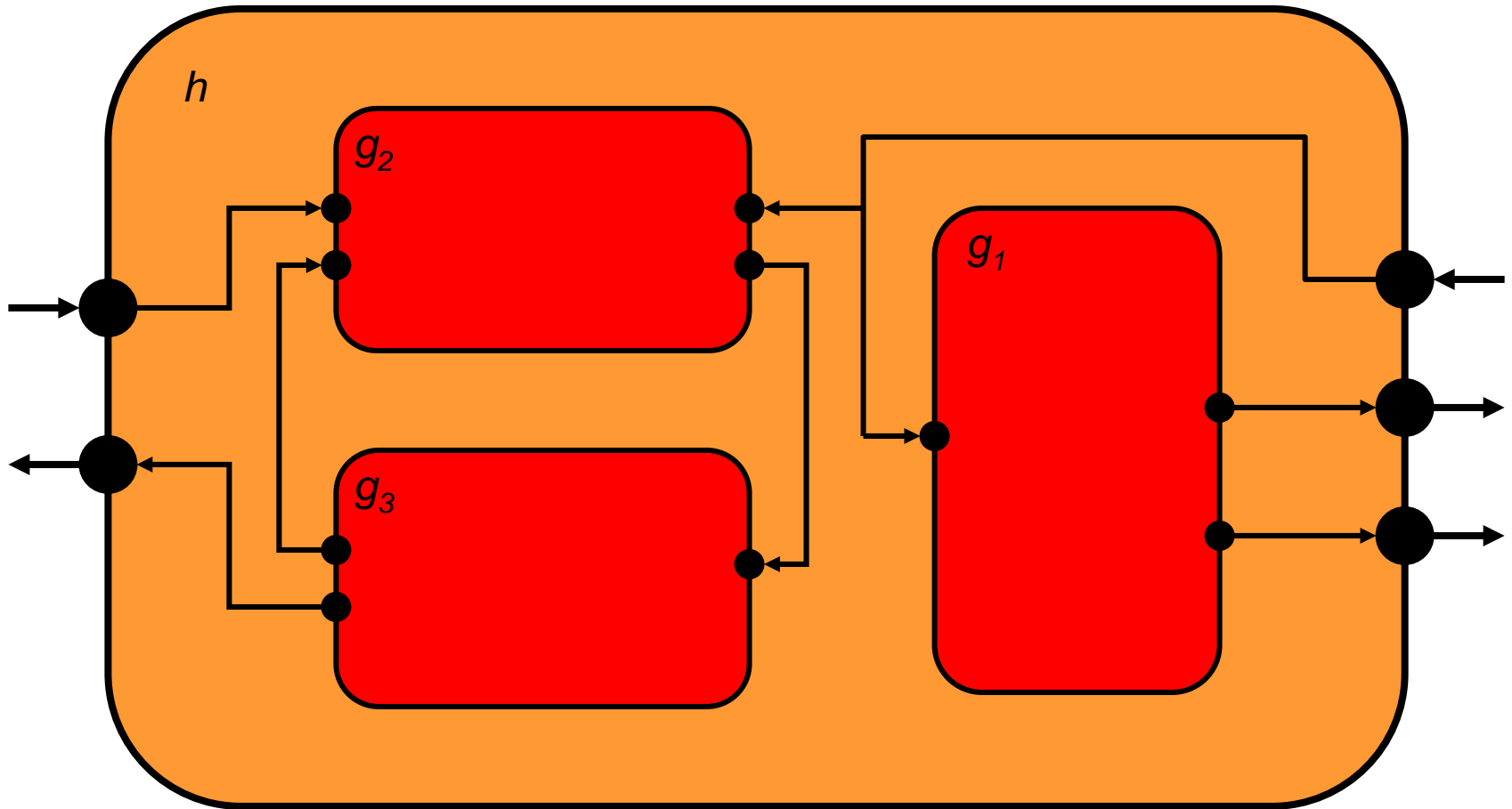
---

$$f_1 \parallel f_2 \parallel f_3 \parallel f_4 \parallel f_5 \parallel f_6 \cdot h$$

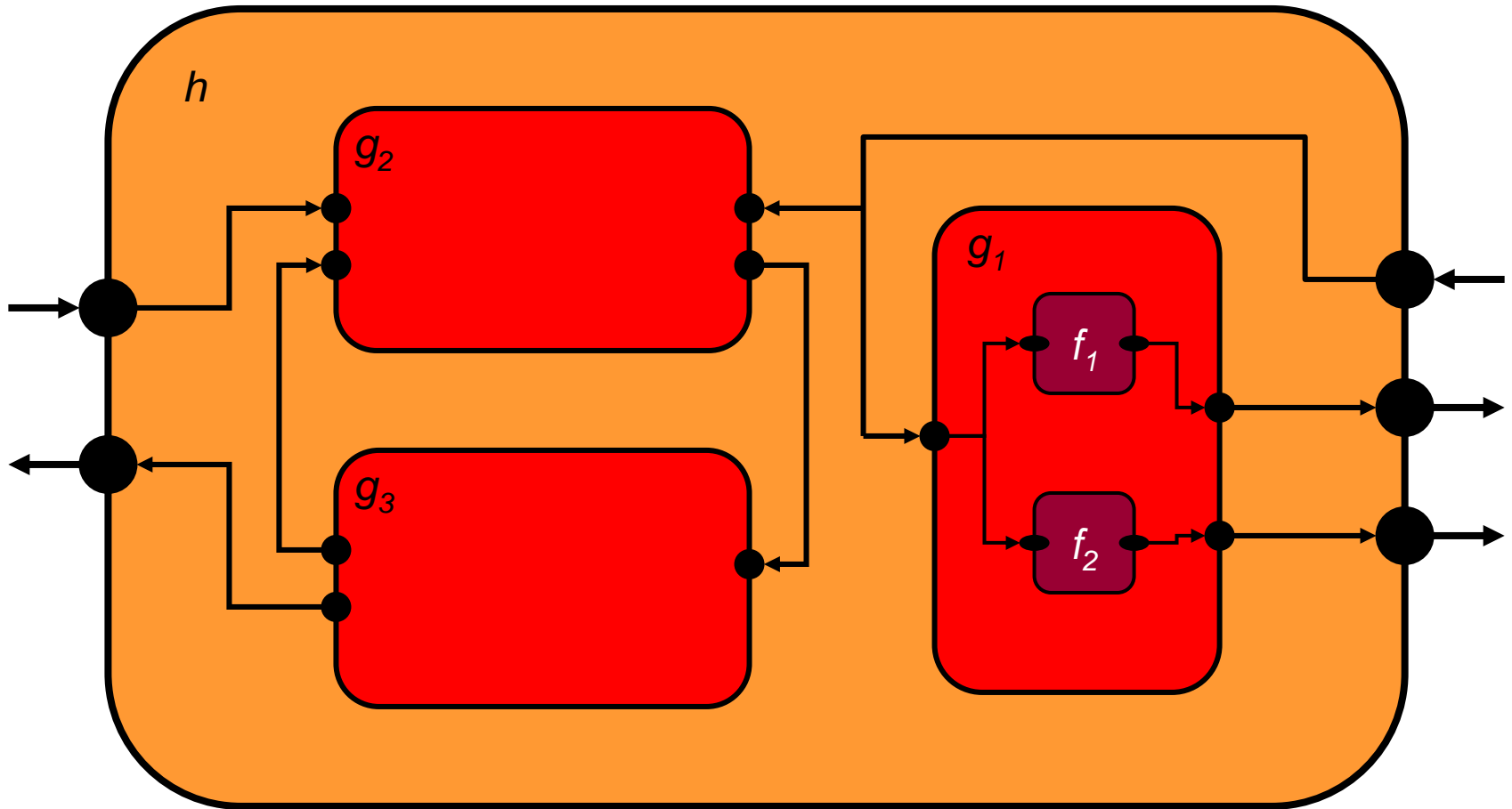
# The Reverse Process



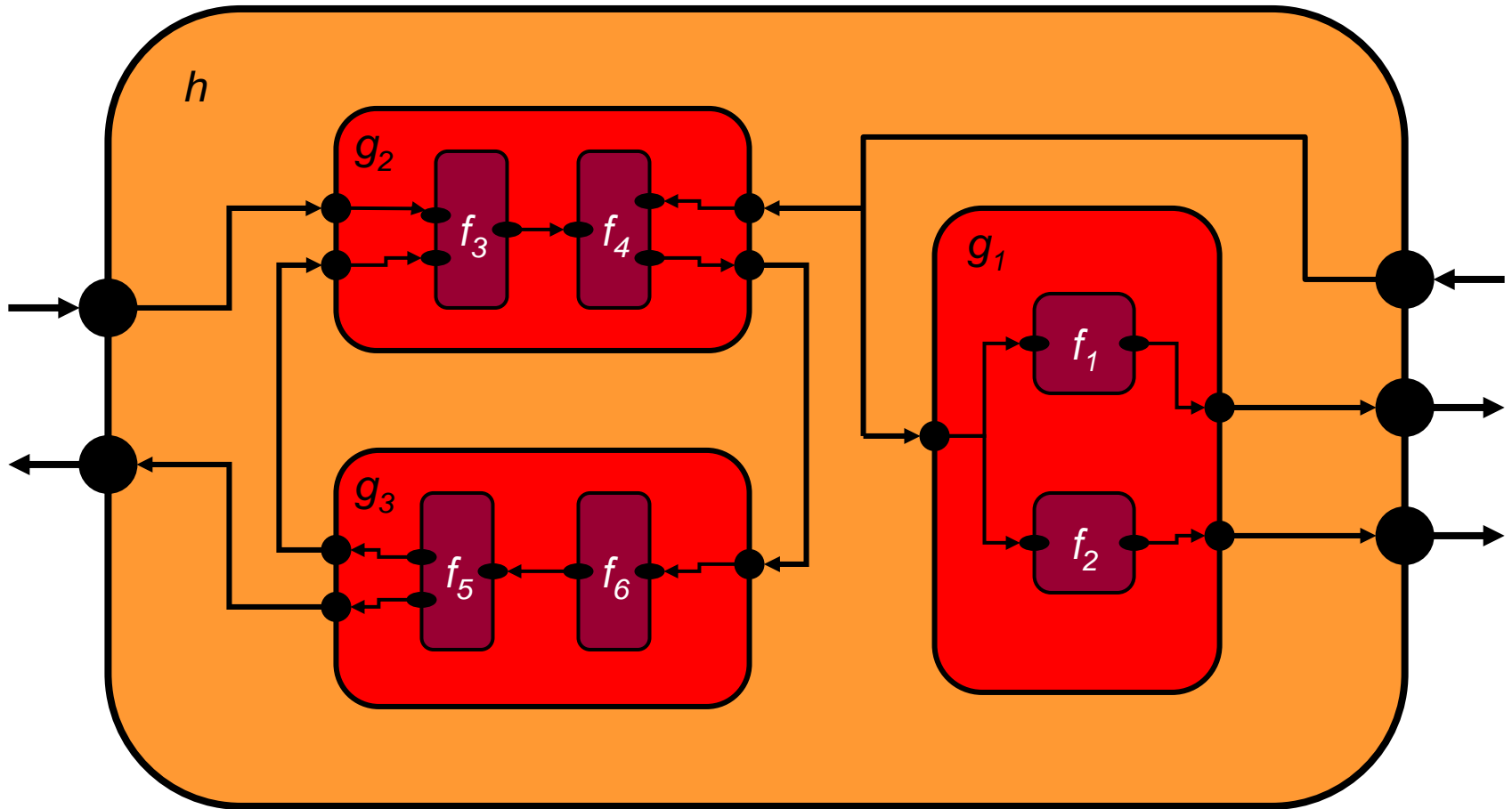
# The Reverse Process: Stepwise decomposition and refinement.



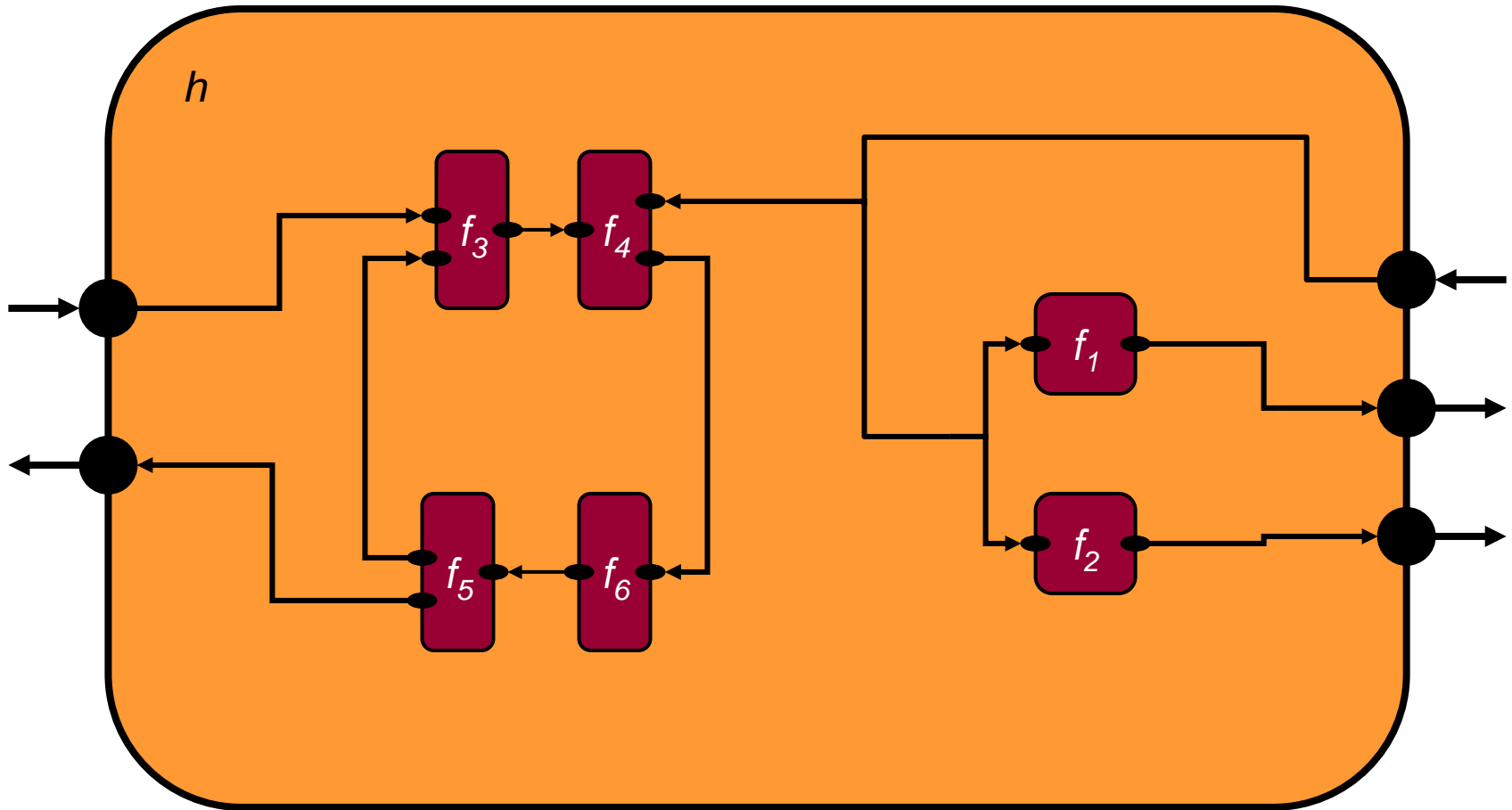
# The Reverse Process: Independent Implementability.



# The Reverse Process: Independent Implementability.



Top-down Compositionality:  
If  $g, f$  and  $g', f'$ , then  $g || g', f || f'$ .



# Top-down Compositionality: Supports Compositional Design.

$h, g_1 || g_2 || g_3$

$g_1, f_1 || f_2$

$g_2, f_3 || f_4$

$g_3, f_5 || f_6$

---

$h, f_1 || f_2 || f_3 || f_4 || f_5 || f_6$



---

Bottom-up Compositionality:

If  $f \cdot g$  and  $f' \cdot g'$ , then  $f//f' \cdot g//g'$ .

---

Top-down Compositionality:

If  $g \text{ , } f$  and  $g' \text{ , } f'$ , then  $g//g' \text{ , } f//f'$ .

---

What's the Difference?

---

Bottom-up Compositionality:

If  $f \cdot g$  and  $f' \cdot g'$ , then  $f // f' \cdot g // g'$ .

---

Top-down Compositionality:

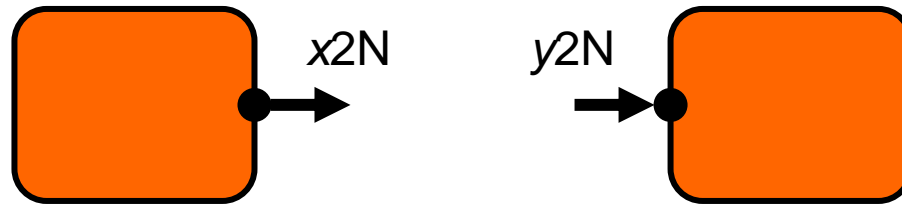
If  $g \cdot f$  and  $g' \cdot f'$ , then  $g // g' \cdot f // f'$ .

---

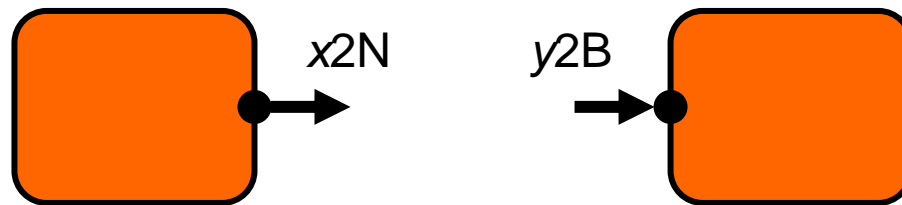
What's the Difference?

If composition is a total function, then there is none.

However, Composition is often Partial.



Compatible.

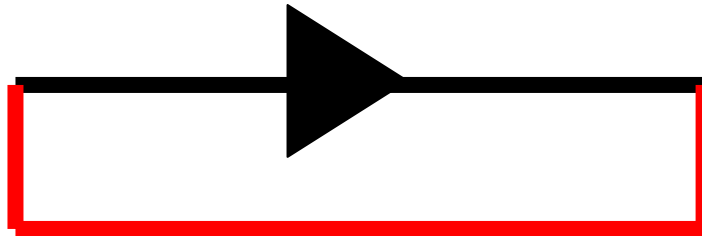


Incompatible.

However, Composition is often Partial.



Compatible.



Incompatible.

# Partial Composition

---

Bottom-up Compositionality:

If  $f//f'$  defined and  $f \cdot g$  and  $f' \cdot g'$ ,  
then  $g//g'$  defined and  $f//f' \cdot g//g'$ .

---

Top-down Compositionality:

If  $g//g'$  defined and  $g \cdot f$  and  $g' \cdot f'$ ,  
then  $f//f'$  defined and  $g//g' \cdot f//f'$ .

---

# Partial Composition

Bottom-up Compositionality:

If  $f//f'$  defined and  $f \cdot g$  and  $f' \cdot g'$ ,  
then  $g//g'$  defined and  $f//f' \cdot g//g'$ .

Processes

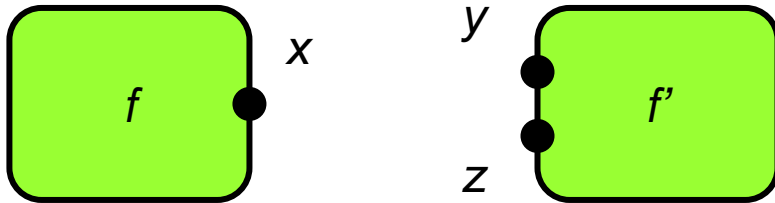
Top-down Compositionality:

If  $g//g'$  defined and  $g \cdot f$  and  $g' \cdot f'$ ,  
then  $f//f'$  defined and  $g//g' \cdot f//f'$ .

Interfaces

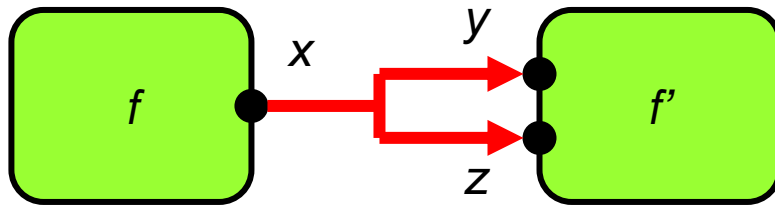
# Block Diagram Algebra

- A set  $P$  of (typed) variables.
- A set  $F$  of *blocks*.
- For each block  $f \in F$ , a set  $P_f \subseteq P$  of *ports*.
- A partial binary function  $//$  on blocks, called *composition*.



# Block Diagram Algebra

- A set  $P$  of (typed) variables.
- A set  $F$  of *blocks*.
- For each block  $f \in F$ , a set  $P_f \subseteq P$  of *ports*.
- A partial binary function  $\parallel$  on blocks, called *composition*.
- A partial function mapping a block  $f \in F$  and an interconnect  $\theta \in P \times P$  to a block  $P \theta$ .



$$\theta = \{ (x,y), (x,z) \}$$



# Block Diagram Algebra

- A set  $P$  of (typed) variables.
- A set  $F$  of *blocks*.
- For each block  $f \in F$ , a set  $P_f \subseteq P$  of *ports*.
- A partial binary function  $//$  on blocks, called *composition*.
- A partial function mapping a block  $f \in F$  and an interconnect  $\theta \in P \times P$  to a block  $P\theta$ .
- A binary relation  $\cdot$  on blocks, called *hierarchy*.

## *Side Conditions on Composition:*

- if  $f//g$  defined, then  $g//f$  defined and equal
- if  $(f//g)//h$  defined, then  $f//(g//h)$  defined and equal
- if  $f//g$  defined, then  $P_{f//g} = P_f [ P_g$

## *Side Conditions on Connection:*

$$I_\theta = \{ x \mid (\exists y)(x,y) \in \theta \}$$

$$O_\theta = \{ y \mid (\exists x)(x,y) \in \theta \}$$

$$p_\theta = \mathbb{A}E_{(x,y) \in \theta} (x=y)$$

-if  $\theta = ;$ , then  $f_\theta$  defined and equal to  $f$

-if  $f_\theta$  defined, then  $P_{f_\theta} = P_f \upharpoonright I_\theta \upharpoonright O_\theta$

## *Side Conditions on Hierarchy:*

*-f · f*

*-if f·g and g·h, then f·h*

A block diagram algebra is an **interface algebra** if

1. if  $g//h$  defined and  $g \circ f$ , then  $f//h$  defined and  $g//h \circ f//h$
2. if  $g\theta$  defined and  $g \circ f$ , then  $f\theta$  defined and  $g\theta \circ f\theta$

A block diagram algebra is an **interface algebra** if

1. if  $g//h$  defined and  $g \cdot f$ , then  $f//h$  defined and  $g//h \cdot f//h$
2. if  $g\theta$  defined and  $g \cdot f$ , then  $f\theta$  defined and  $g\theta \cdot f\theta$

A block diagram algebra is a **process algebra** if

1. if  $f//h$  defined and  $f \cdot g$ , then  $g//h$  defined and  $f//h \cdot g//h$
2. if  $f\theta$  defined and  $f \cdot g$ , then  $g\theta$  defined and  $f\theta \cdot g\theta$

# Stateless Input/Output Processes

$$f = (I_f, O_f, p_f)$$

$I_f \subseteq P \dots$  input ports

$O_f \subseteq P \setminus I_f \dots$  output ports

$p_f \dots$  input/output relation: predicate on  $I_f \times O_f$   
such that  $(\exists I_f)(\exists O_f) p_f$

# Stateless Input/Output Processes

$$f = (I_f, O_f, p_f)$$

$$g = (I_g, O_g, p_g)$$

$$P_f = I_f [ O_f$$

$f \parallel g$  defined if  $P_f \dot{\wedge} P_g = ;$

Then:

$$I_{f \parallel g} = I_f [ I_g$$

$$O_{f \parallel g} = O_f [ O_g$$

$$p_{f \parallel g} = p_f \dot{\wedge} p_g$$



$$f = (I_f, O_f, p_f)$$

$$\theta \mu I_\theta \xi O_\theta$$

$f\theta$  defined if

$$1. I_f \dot{\wedge} I_\theta = ;$$

$$2. O_f \dot{\wedge} O_\theta = ;$$

$$3. (8 I_{f\theta})(9 O_{f\theta}) p_{f\theta} \quad (\text{as defined below})$$

Then:

$$I_{f\theta} = (I_f [ I_\theta) \setminus O_{f\theta}$$

$$O_{f\theta} = O_f [ O_\theta$$

$$p_{f\theta} = p_f \text{ \AE } p_\theta$$

$$f = (I_f, O_f, p_f)$$

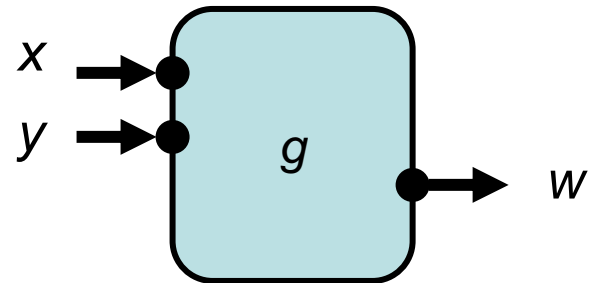
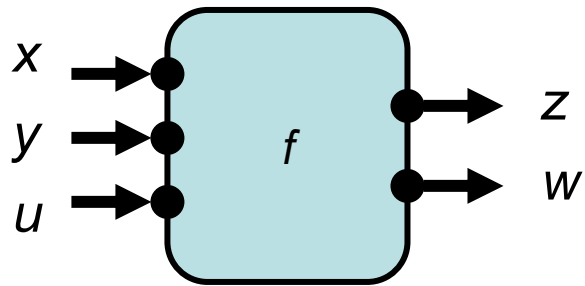
$$g = (I_g, O_g, p_g)$$

$f \cdot g$  if

1.  $I_f \supseteq I_g$
2.  $O_f \supseteq O_g$
3.  $p_f \supseteq p_g$

*abstraction has fewer inputs*

*and fewer outputs*



$$z = x + y \text{ } \forall$$

$$(u=0) \ w=x \text{ } \forall$$

$$(u \neq 0) \ w=y$$

$$w=x \ \S \ w=y$$

Why is this a process algebra?

1. if  $I_f \dot{\sim} I_\theta = ;$  and  $I_f \parallel I_g$ , then  $I_g \dot{\sim} I_\theta = ;$
2. if  $O_f \dot{\sim} O_\theta = ;$  and  $O_f \parallel O_g$ , then  $O_g \dot{\sim} O_\theta = ;$
3. if  $(\exists I_{f\theta})(\exists O_{f\theta})(p_f \text{AE } p_\theta)$  and  $p_f \parallel p_g$  and  $I_{f\theta} \parallel I_{g\theta}$  and  $O_{f\theta} \parallel O_{g\theta}$ ,  
then  $(\exists I_{g\theta})(\exists O_{g\theta})(p_g \text{AE } p_\theta)$

## *Stateless Input/Output Interfaces*

$$f = (I_f, O_f^+, O_f)$$

$$O_f \mu O_f^+$$

$$I_f \dot{\Delta} O_f^+ = ;$$

$O_f^+$  ... set of *available* output port names

$$P_f = I_f [ O_f$$

$$P_f^+ = I_f [ O_f^+$$

## *Stateless Input/Output Interfaces*

$$f = (I_f, O_f^+, O_f)$$

$$g = (I_g, O_g^+, O_g)$$

$f \parallel g$  defined if  $P_f^+ \dot{\wedge} P_g^+ = ;$

Then:

$$I_{f \parallel g} = I_f [ I_g$$

$$O_{f \parallel g}^+ = O_f^+ [ O_g^+$$

$$O_{f \parallel g} = O_f [ O_g$$

$$f = (I_f, O_f^+, O_f)$$

$$\theta \mu I_\theta \text{ } \mathcal{L} O_\theta$$

$f_\theta$  defined if

1.  $I_\theta \mu O_f$

2.  $O_\theta \dot{\Delta} O_f^+ = ;$

3. if  $(x, y), (x', y') \in \theta$  and  $x \neq x'$ , then  $y \neq y'$

Then:

$$I_{f_\theta} = I_f \setminus O_\theta$$

$$O_{f_\theta}^+ = O_f^+ \setminus O_\theta$$

$$O_{f_\theta} = O_f \setminus O_\theta$$

$$f = (I_f, O_f^+, O_f)$$

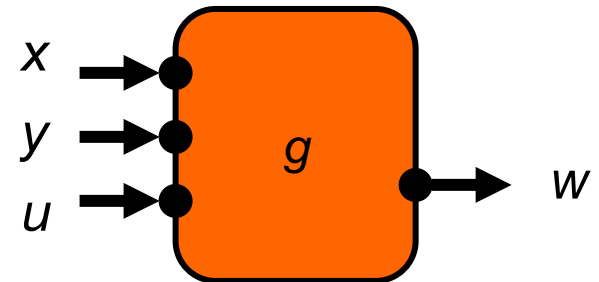
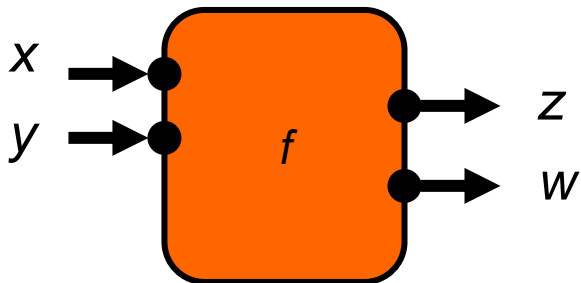
$$g = (I_g, O_g^+, O_g)$$

$f \cdot g$  if

1.  $I_f \mu I_g$
2.  $O_f^+ \mu O_g^+$
3.  $O_f \uparrow O_g$

*refinement has **fewer** inputs*

*and **more** outputs*





$$f = (I_f, O_f^+, O_f)$$

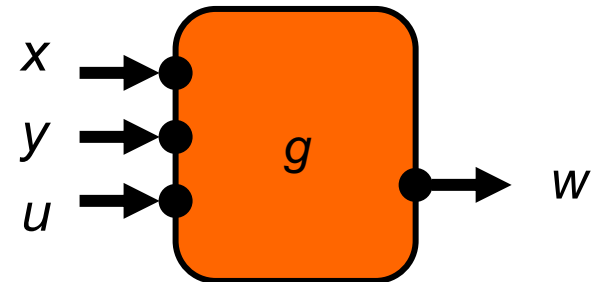
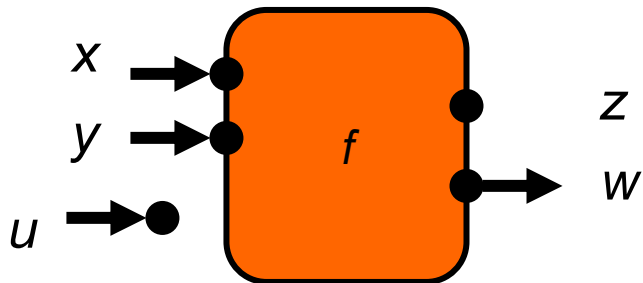
$$g = (I_g, O_g^+, O_g)$$

$f \cdot g$  if

1.  $I_f \mu I_g$
2.  $O_f^+ \mu O_g^+$
3.  $O_f \uparrow O_g$

*refinement has **fewer** inputs*

*and **more** outputs*



Why is this an interface algebra?

1. if  $P_g^+ \dot{\wedge} P_h^+ = ;$  and  $P_g^+ \dot{\parallel} P_f^+$ , then  $P_f^+ \dot{\wedge} P_h^+ = ;$
2. if  $I_\theta \mu O_g$  and  $O_g \mu O_f$ , then  $I_\theta \mu O_f$
3. if  $O_g^+ \dot{\wedge} O_\theta = ;$  and  $O_g^+ \dot{\parallel} O_f^+$ , then  $O_f^+ \dot{\wedge} O_\theta = ;$

# *Stateless Assume/Guarantee Interfaces*

$$f = (I_f, O_f^+, O_f, in_f, out_f)$$

$in_f$  ... *input assumption*: satisfiable predicate on  $I_f$

$out_f$  ... *output guarantee*: satisfiable predicate on  $O_f$

# Stateless Assume/Guarantee Interfaces

$$f = (I_f, O_f^+, O_f, in_f, out_f) \quad g = (I_g, O_g^+, O_g, in_g, out_g)$$

$f \parallel g$  defined if  $P_f^+ \dot{\wedge} P_g^+ = ;$

Then:

$$in_{f \parallel g} = in_f \dot{\wedge} in_g$$

$$out_{f \parallel g} = out_f \dot{\wedge} out_g$$

$$f = (I_f, O_f^+, O_f, in_f, out_f)$$

$$\theta \mu I_\theta \text{ } \mathcal{E} O_\theta$$

$f\theta$  defined if

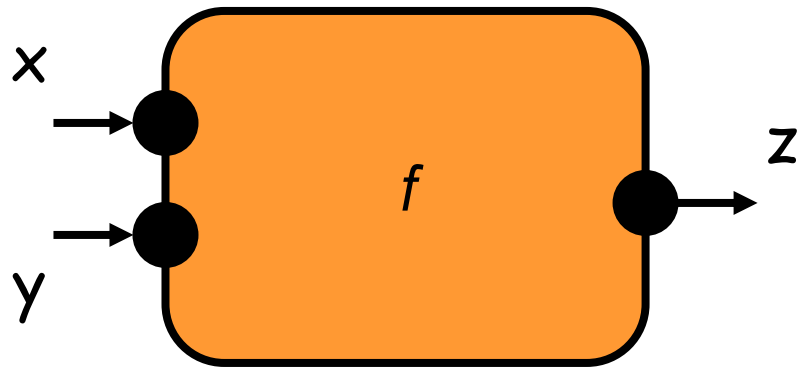
1-3 as before

4.  $in_{f\theta}$  satisfiable (as defined below)

Then:

$$in_{f\theta} = (8 O_{f\theta})(out_{f\theta} \mathcal{E} p_\theta) in_f$$

$$out_{f\theta} = (9 I_{f\theta})(out_f \mathcal{E} p_\theta)$$



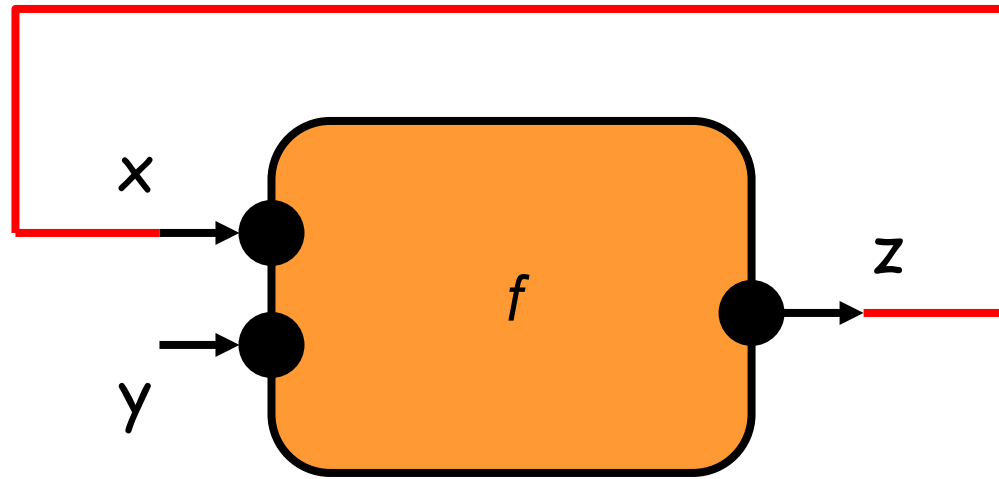
$x=0 \Rightarrow y=0$

true

Input assumption

Output guarantee

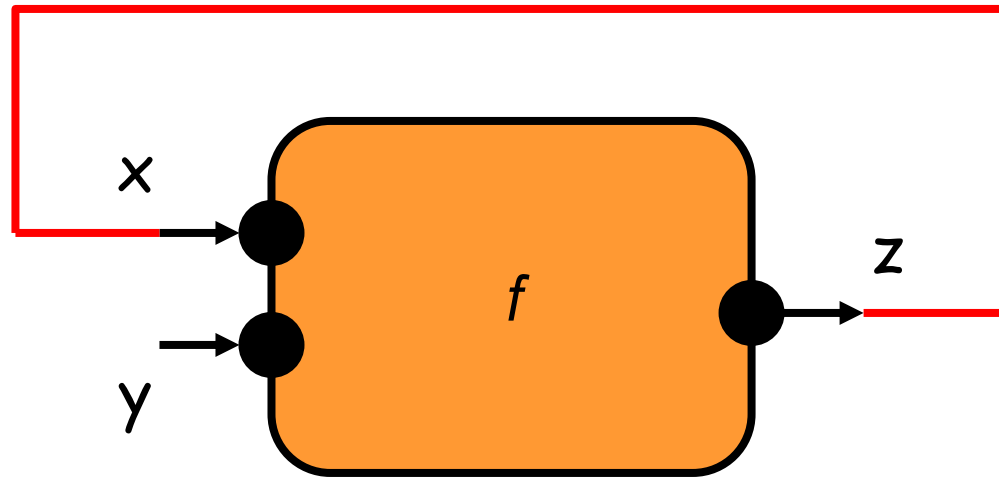
$$\theta = \{(z,x)\}$$



$$x=0 \Rightarrow y=0$$

true

$$\theta = \{(z,x)\}$$



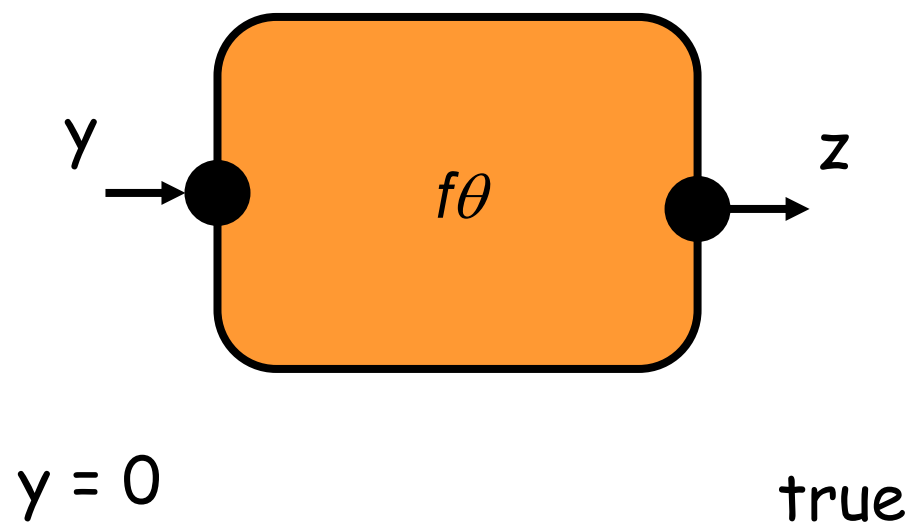
$$x=0 \Rightarrow y=0$$

true

$$y = 0$$

$$(\forall x,z) ((\text{true} \wedge x=z) \Rightarrow (x=0 \Rightarrow y=0))$$





$$f = (I_f, O_f^+, O_f, in_f, out_f)$$

$$g = (I_g, O_g^+, O_g, in_g, out_g)$$

$f \cdot g$  if

1-3 as above

4.  $in_f$  (  $in_g$

5.  $out_f$  )  $out_g$

Next Lecture:

We will add *state*.

# *What is the Compositionality of your Favorite Model ?*

## Bottom-up Compositionality:

If  $f//f'$  defined and  $f \cdot g$  and  $f' \cdot g'$ ,  
then  $g//g'$  defined and  $f//f' \cdot g//g'$ .

Processes

## Top-down Compositionality:

If  $g//g'$  defined and  $g, f$  and  $g', f'$ ,  
then  $f//f'$  defined and  $g//g', f//f'$ .

Interfaces

*“defined” could be typeable, deadlock-free, etc.*

## Lesson 1:

*Never dogmatically believe in one particular model / formalism / language.*

*Always evaluate your choices for your given special situation.*