

Using and Building an Automatic Program Verifier

Rustan Leino

Microsoft Research, Redmond, USA

These lectures give a taste of what current technology can do for program correctness.

As computer scientists, we sometimes find the need to reason rigorously about the correctness of our programs. Such reasoning can be assisted by an automatic program verifier. One of the aims in these lectures is to teach concepts needed to use a program verifier and to give students some experience with a program verifier (namely, with the language and verifier Dafny). This will start with basic ideas like procedure specifications and loop invariants, and cover the more advanced topic of how to specify data-structure invariants in heap-manipulating programs. The take-home message is for students to remember verification and verification tools the next time a program is not entirely obvious.

As researchers in field of software correctness, we often find the need to build some program-analysis tool, be it a simple checker or a full-blown program verifier. Satisfiability-modulo-theories (SMT) solvers provide automated reasoning power for many common domains, and therefore make a useful component of many program analyses. To build a tool that uses an SMT solver also requires a lot of plumbing, plumbing that is shared among many similar analysis tools. The second aim of these lectures is to teach theoretical foundations of this shared infrastructure, described in terms of an intermediate verification language (namely, the language and verification engine Boogie). The lectures will also teach the main ideas behind the translation of an analysis problems into Boogie. The take-home message is: don't build your plumbing from scratch, leverage an intermediate verification language.

References

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